2F04079-PCT

FIG.3

301 LOGIC CIRCUIT

MASK

303 BIT MASK CIRCUIT

FIG.7

151: CELL B1

FIG.8

152: CELL B2

FIG.9

153: CELL B3

FIG.13

151: CELL B1

FIG.14

152: CELL B2

FIG.15

153: CELL B3

FIG.16

154: CELL B4

FIG.18

CONNECTED TO CARRY NETWORK
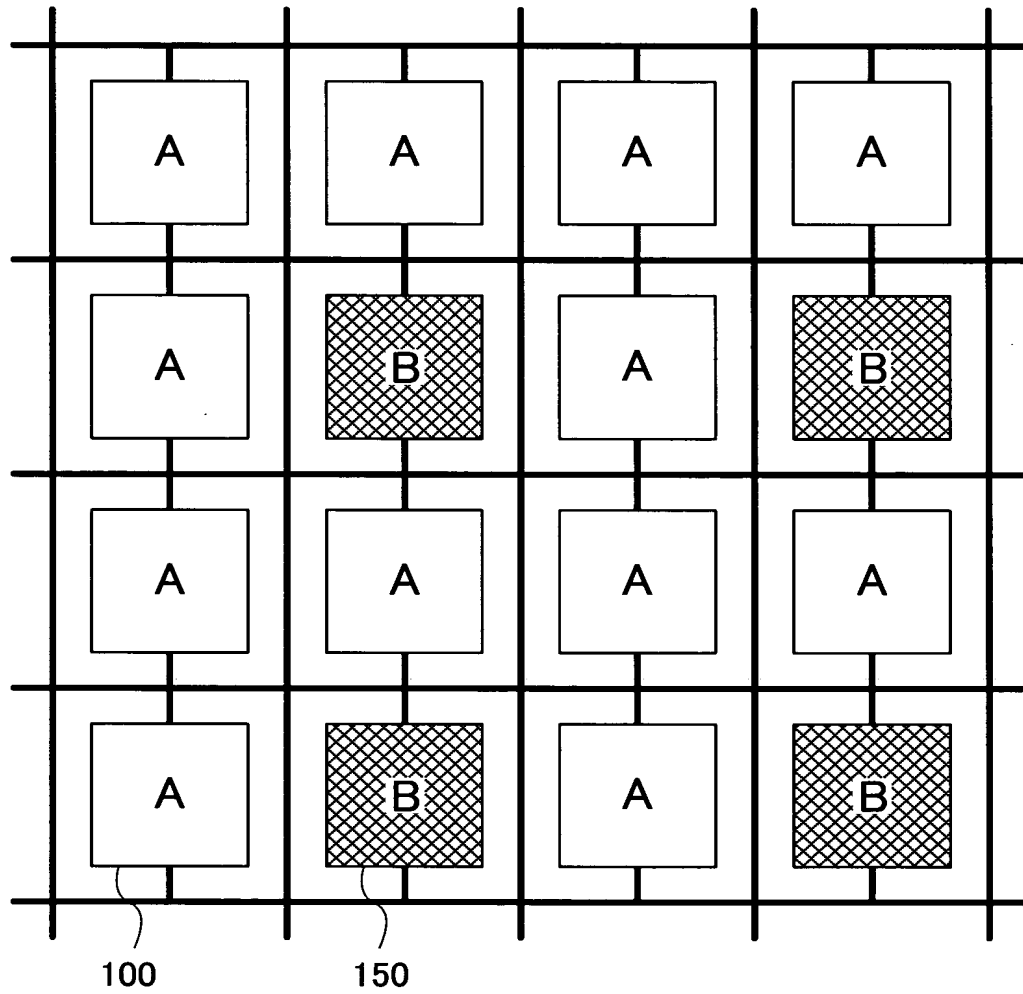
301 LOGIC CIRCUIT

MASK

303 BIT MASK CIRCUIT

FIG.1

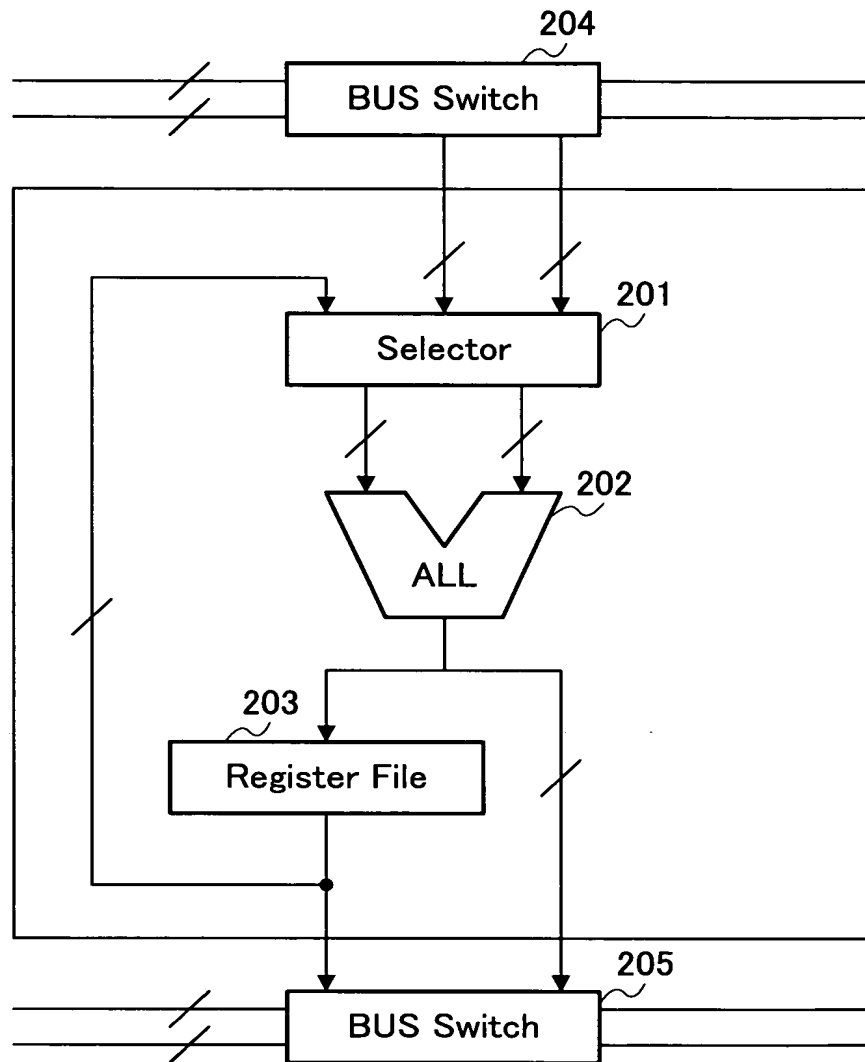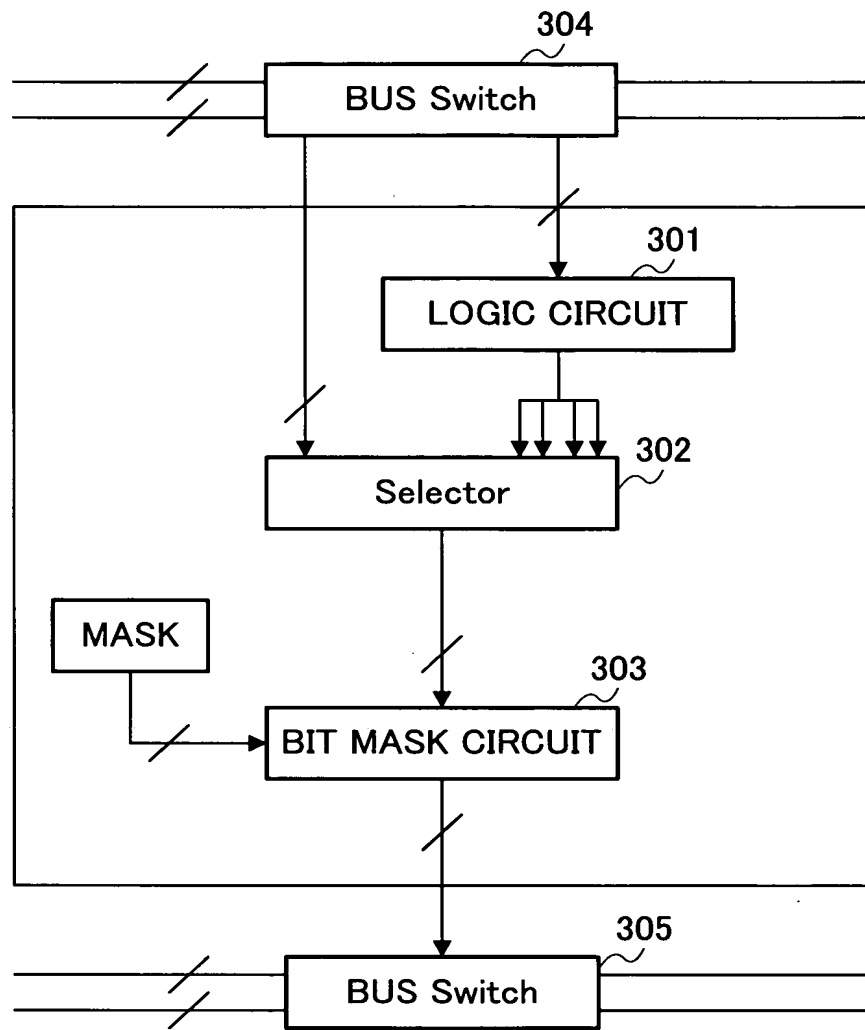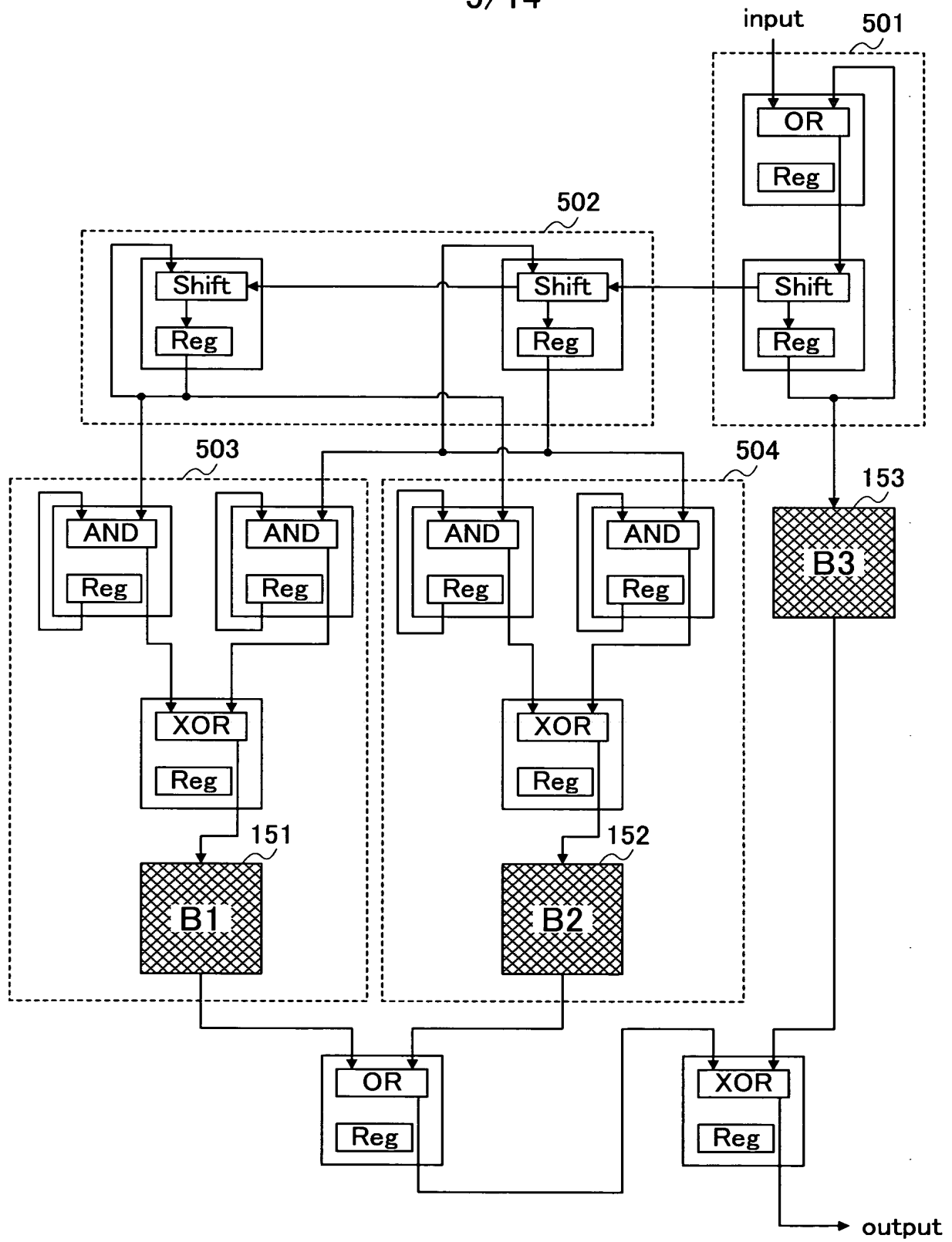FIG.2

FIG.3

FIG.4

input

501

OR

Reg

502

Shift

Reg

Shift

Reg

Shift

Reg

503

504

AND

Reg

AND

Reg

AND

Reg

AND

Reg

153

B3

XOR

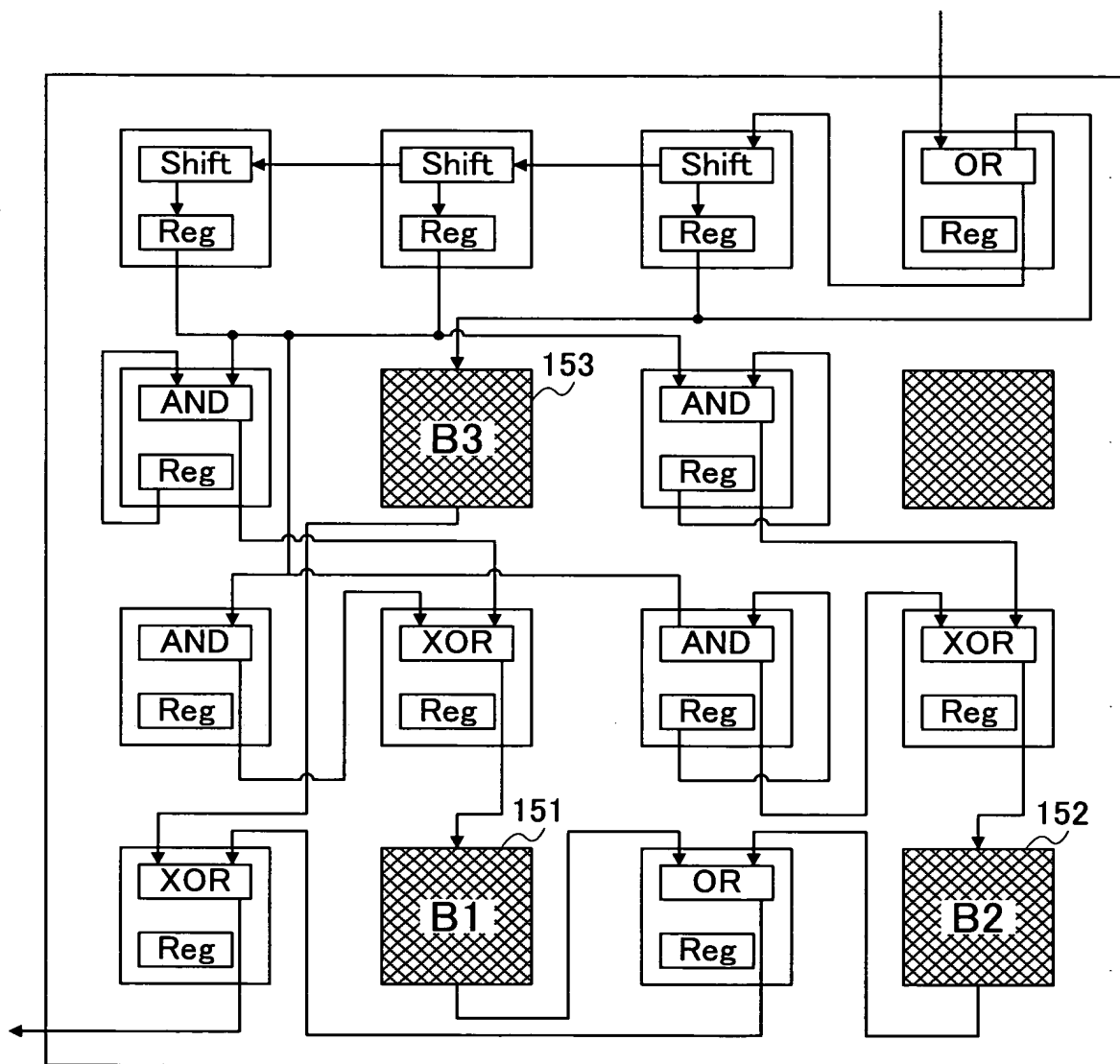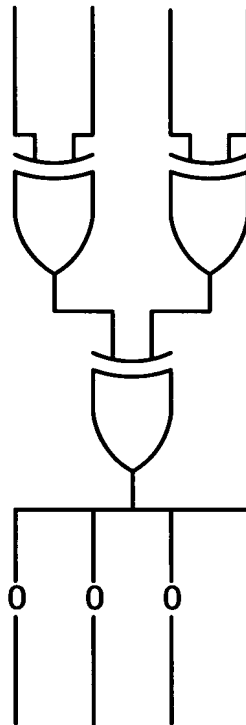Reg

XOR

Reg

151

152

B1

B2

OR

Reg

XOR

Reg

output

FIG.5

FIG.6

151: CELL B1

FIG.7

152: CELL B2

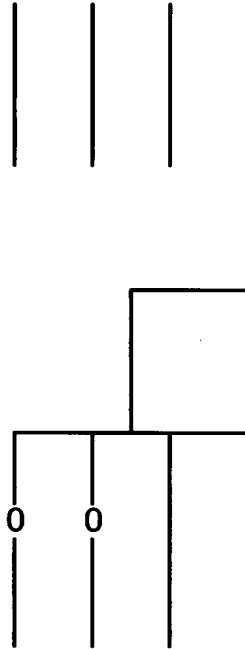FIG.8

153: CELL B3

FIG.9

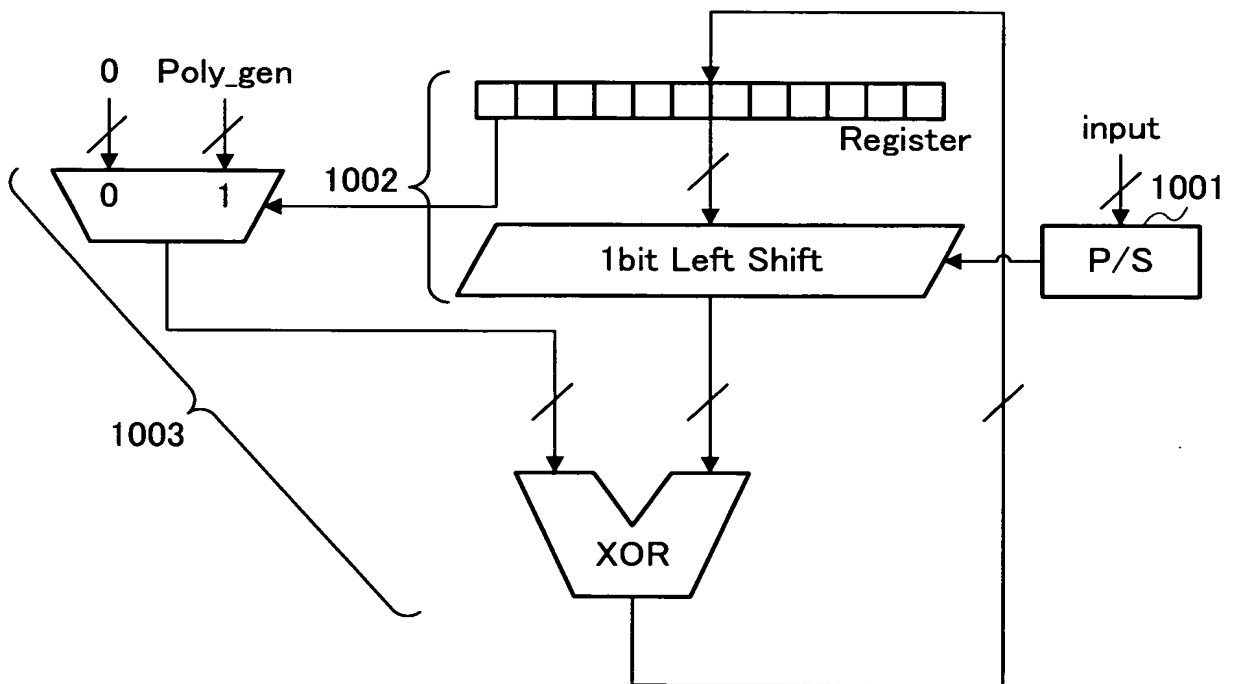0   Poly_gen

0   1

1002

1003

Register

input

1001

1bit Left Shift

P/S

XOR
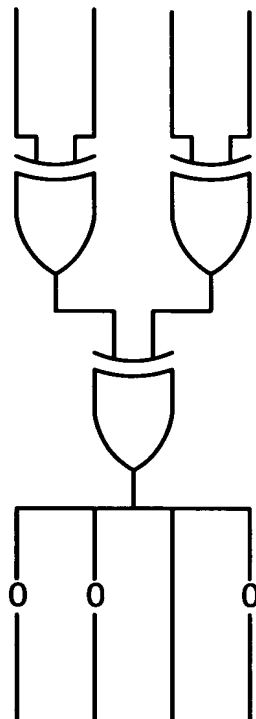
FIG.10

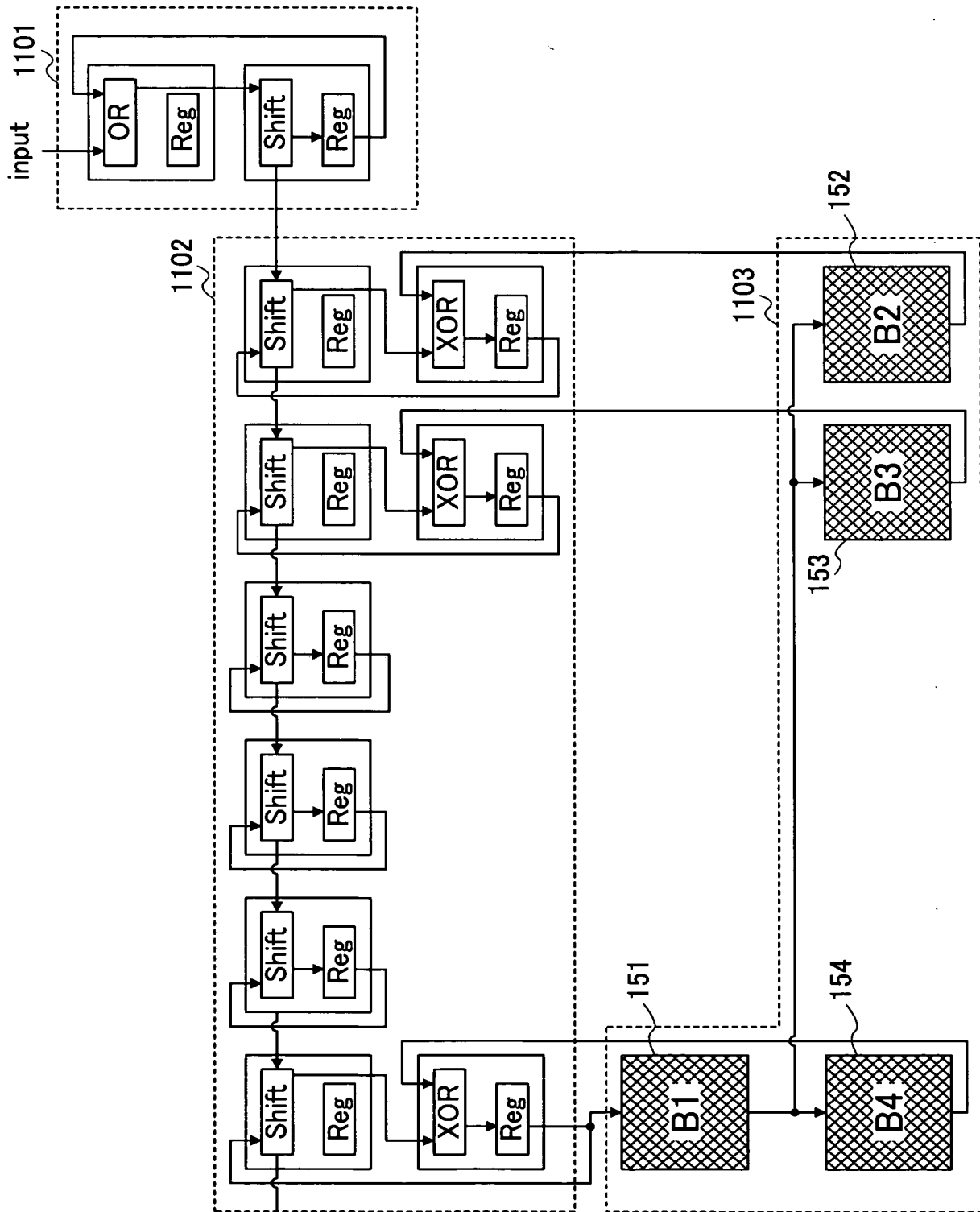FIG.11
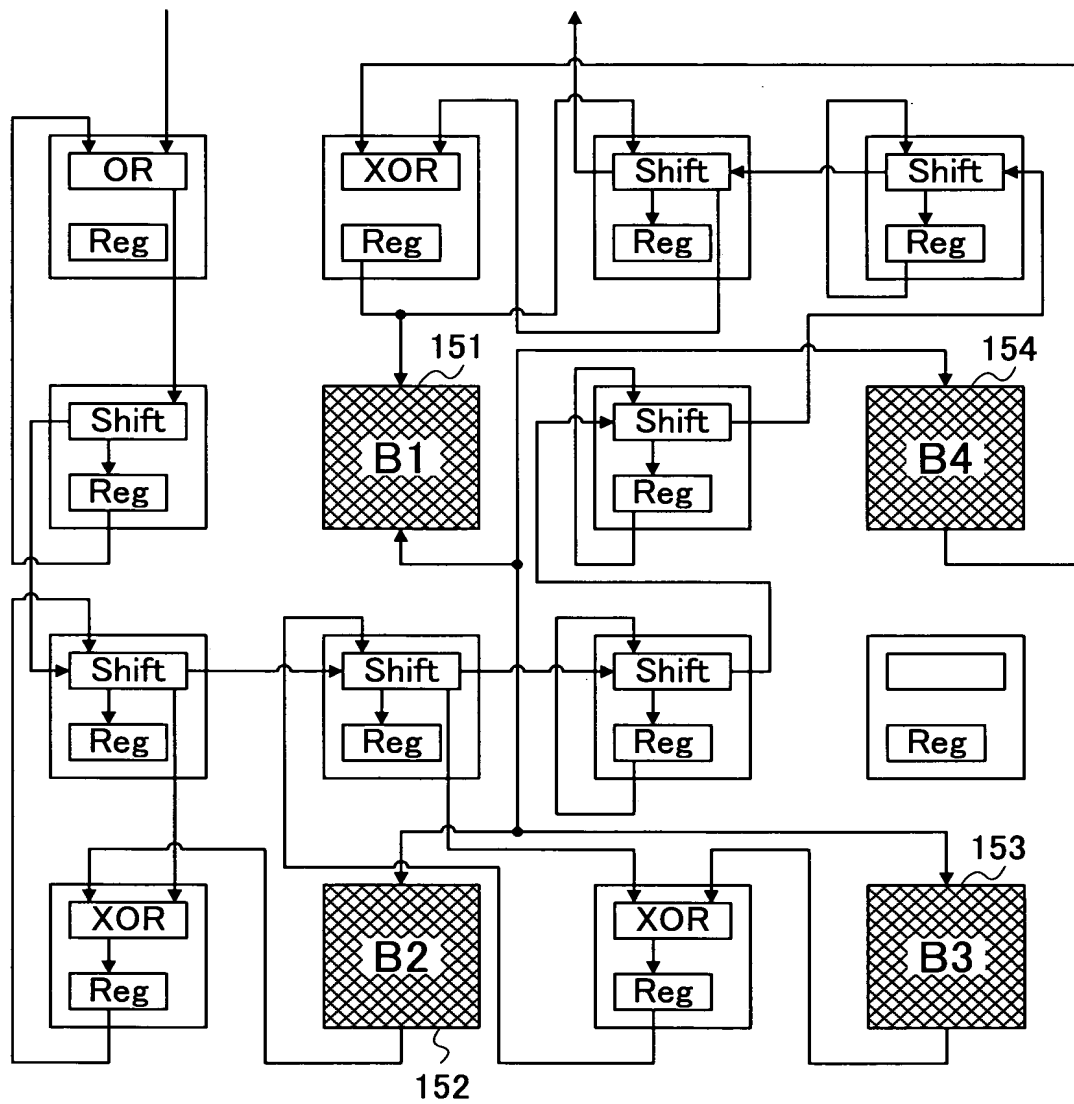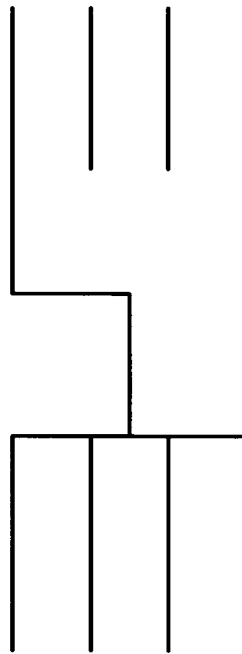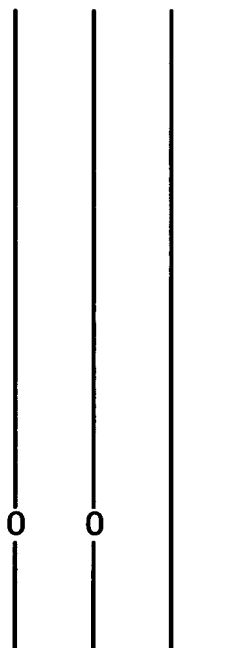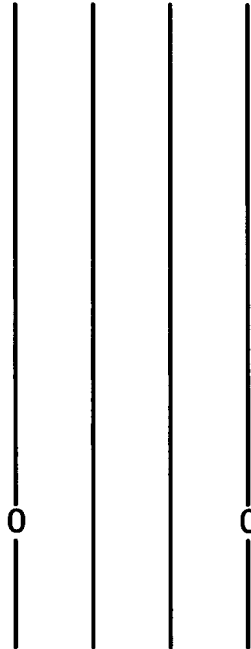
FIG.12

151: CELL B1

FIG.13

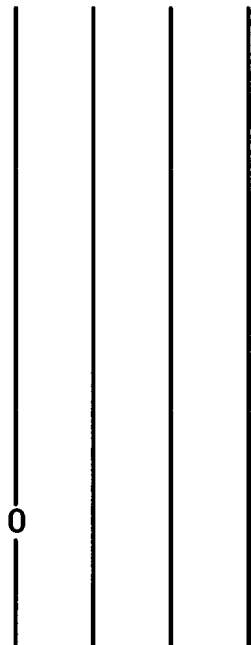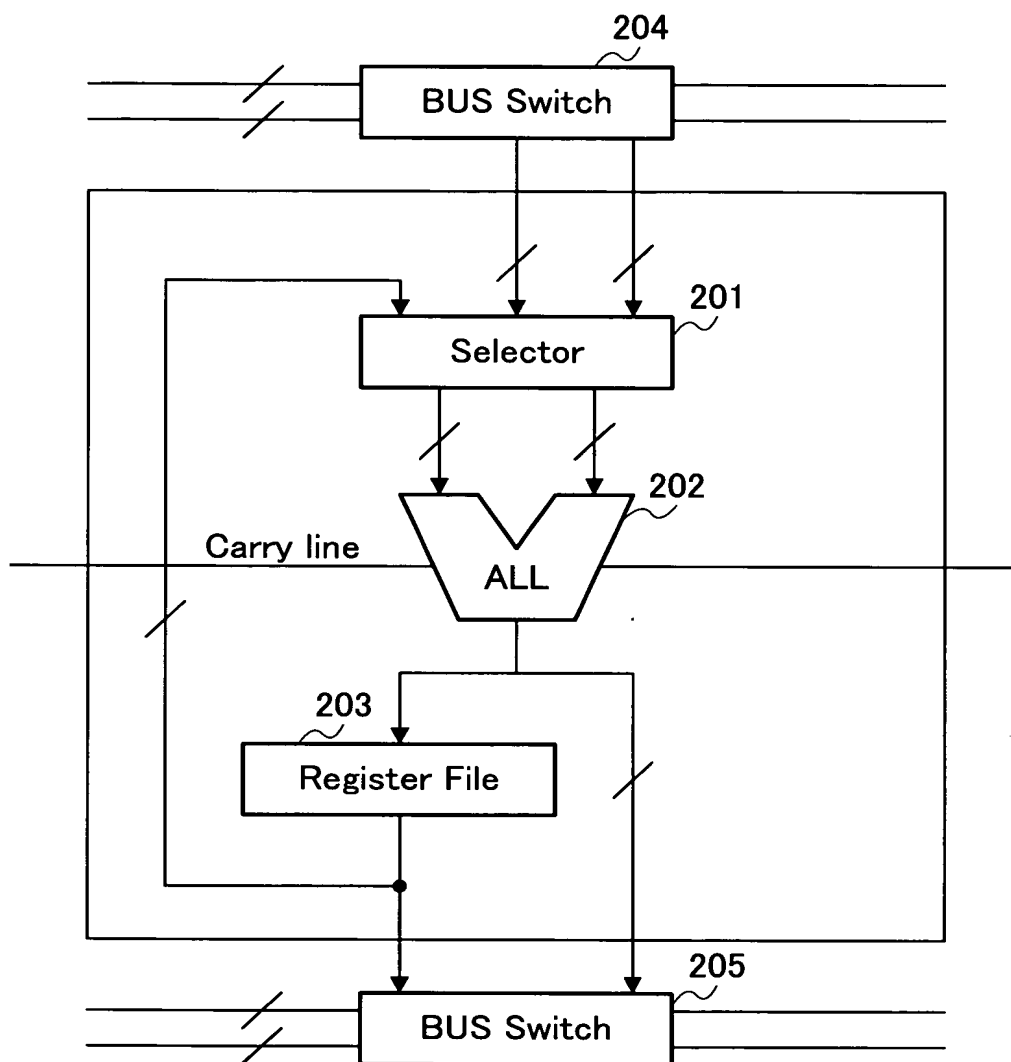152: CELL B2

FIG.14

153: CELL B3

FIG.15

154: CELL B4

FIG.16

FIG.17

FIG.18